

ISSUE CLASSIFICATION	
Class	Subclass

**U.S. UTILITY Patent Application**

O.I.P.E. PATENT DATE  
 SCANNED 15-4 Q.A. Am

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/853998	F	714	733	2133	Derack

APPLICANTS

Tsuruto Matsui

TITLE

Pattern generator for semiconductor test system

PTO-2040  
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			_____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____	_____ (Primary Examiner)      (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      (Date)		<b>ISSUE BATCH NUMBER</b>	

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